The Design and Construction of LHCb VELO Upgrade Modules

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Abstract—The construction of the new LHCb Vertex Locator (VELO) detector is presented. The upgraded subsystem will play a crucial role in the tracking during data-taking runs starting in 2021, its main objective locating primary and secondary vertices. Compared to its predecessor, the main advantages are better resolution together with trigger-less readout at the maximal rate of 40 MHz. In total, VELO consists of 52 modules positioned in vacuum along the LHC beam-pipe, surrounding the interaction point. The modules are populated with 4 hybrid silicon pixel detectors with pixel pitch of 55 μm. Each of the sensors is read out by 3 VeloPix ASICs with 256x256 pixels. For experiment control and data propagation, sets of front-end hybrids and GBTx ASICs are utilized. The data are then sent through a vacuum feed-through board to an opto-and-power (OPB) board, which is connected to the rest of the experiment via optical fibres. Cooling of the whole module is achieved by phase transition of liquid CO₂ using a custom-made silicon micro-channel substrate.

The assembly of modules at both University of Manchester (Manchester, UK) and Nikhef (Amsterdam, NL) requires high precision in many aspects, therefore extensive procedures for the large-scale construction and its quality assurance have been deployed. The information during each step is uploaded to the online database and automatically analyzed, providing instantaneous information about quality of both components, performed tasks and whole modules. Final assembly of the whole system then takes place at University of Liverpool (Liverpool, UK) and is then transported to CERN (Geneva, CH).

Index Terms—LHC, hybrid pixel detectors, radiation resistant, tracking systems

I. INTRODUCTION

LHCb is a complex detector system operated at one of the interaction points of LHC at CERN. The detector is a single arm forward spectrometer, its main purpose is to study CP violation, rare decays and more, focusing on b- and c-hadron decays [1]. The schematic layout of the LHCb is in figure 1.

After a successful 10 year-long operation and delivered integrated luminosity of 10 fb⁻¹, the system is being upgraded with novel sub-detectors. The upgrade is occurring during second Long Shutdown over a period of two years 2019-2020 with commissioning during first half of the year 2021.

The VELO (VErtex LOcator) is its crucial system used for tracking and locating vertices, the original system consisted of silicon strip detectors, arranged to determine r and φ coordinates of the passing particles. It was the system closest to, and surrounding, the interaction point [3], [4]. The new system consists of silicon pixel detectors, increasing the effective number of channels from 180k up to almost 41M. In addition, whereas the previous VELO was triggered with 1 MHz readout, the new system is readout untriggered at the beam-crossing frequency of 40 MHz [2]. Simulations of both resolution and efficiency for old and upgrade VELO are in figure 2.

II. MODULE DESIGN

In total, the VELO upgrade consists of 52 modules positioned in vacuum along the LHC beam-pipe, surrounding the interaction point [5]. For the best tracking performance the smallest possible amount of material has to be used in constructing all components. This also helps lowering radiation damage of the sensitive parts.
The VELO upgrade modules are populated with 4 hybrid silicon pixel detectors with pixel pitch of 55 µm. Each of the sensors is read out by 3 VeloPix ASICs with 256x256 pixels [6]. HV for biasing is delivered through thin HV tapes to sensors, operated up to maximum of 1000 V.

For experiment control and data propagation, sets of front-end hybrids and GBTx ASICs are utilized. The data are then sent through a vacuum feed-through board to an OPB (Opto-and-Power Board), which is connected to the rest of the experiment via optical fibres. Cooling of the whole module is achieved by evaporation of liquid CO₂ operated at −30 °C, using a custom-made silicon micro-channel substrate [7].

In order to operate the detector in vacuum conditions close to the interaction point without possible contamination of primary beam-pipe vacuum, a thin aluminium RF foil is used for vacuum separation and protection from beam-induced effects [2].

An example of a final design of a fully assembled module is in figure 3.

III. MODULE CONSTRUCTION

The assembly of the modules consists of many different steps, requiring high precision at each one. At first, the bare module consisting of aluminium foot, carbon fibre legs, mid-plate and the cooling substrate is constructed. Afterwards, tiles and hybrid ASICs are glued, wire-bonded and the rest of the flat cables for communication and HV are attached.

A more detailed description of some of the most critical assembly steps follows.

A. Tiles Alignment

Placement of the tiles on the modules is one of the most precise tasks during module construction, aiming at achieving a precision better than 30 µm. Alignment is done using custom-built mechanical jigs, a reference frame is measured with respect to the module foot.

Each tile is placed on a dedicated vacuum mount equipped with 2 independent translation stages and a rotation stage. A camera mounted on the motion system allows for pattern recognition and position measurement of the alignment markers etched on the ASICs during fabrication.

B. Gluing

For the best cooling performance delivered to all VeloPixes, a thin uniform layer of glue has to be deposited. Stycast 2850FT with catalyst 9 were selected for this purpose [8]. The hybrid ASICs attached using a standard loctite glue, HV tapes using a drop of Araldite 2020 and wire-bonding.

The aligned tiles are transferred using a vacuum holder to the gluing robot, the glue deposition is then performed automatically.

The glue layer has to not only be uniform at around 80 µm but also trapping of air has to be avoided, as the expansion of the trapped air pockets during the detector operation in vacuum could cause damage. For this purposes, star shaped pattern has been tested and selected. Compared to other options (e.g. a snake-like pattern), there is a smaller chance to trap air or spill glue over the edge due to the smaller deposited amount in those regions, without reducing the overall coverage of the ASIC. The comparison of two patterns deposited is in figure 4.

C. Wire-bonding

Each of the 4 tiles must be wire bonded to its associated hybrid circuit, with a total of 420 bonds per tile. The procedure is done separately for the two sides, connection of ASICs to the front-end hybrids and HV tapes to the sensor surface in one go.
IV. Quality Assurance

Every component is graded when produced, however, the final quality of the module might be lowered by an imprecision during any of the assembly steps. Because of that, a variety of checks are performed over the course of the assembly, split into three categories: metrology (measurements of building precision), mechanical performance (effects of high voltage and cooling) and communication (data acquisition chain tests). Each test is then assigned a grade from a list: A - production grade; B - insignificant issues; C - moderate issues; F - QA failure.

The same set of tests is done at both assembly sites (Manchester and Nikhef) and CERN, differing only in some of the tools and data formats used. Some of the modules have been tested at all three places to cross-verify the setups and minimize the errors from production.

A. Metrology

1) Substrate flatness: Flatness measurement of the cooling substrate is performed in order to check the quality and spot any damage that could have happened during production, transport or storage of the substrates. It is performed both at the cooling substrate production site (CERN) and at each of the assembly sites.

Different tools are used for the measurement, relying on optical or laser systems. Values of $x$, $y$, and $z$ are obtained, $\Delta z$ is then obtained by plane fitting. A comparison between measurement performed at CERN and at Manchester is in figure 5. The flatness measurements are in correlation (as seen from the heat-map and violin plot distribution). Both measurements indicate a peak in bottom left corner, corresponding to the known crack in the micro-channel plate for that particular low-grade substrate.

Grading of the measurement is defined using $\eta_{25}$ and $\eta_{75}$ of $\Delta z$ values, results being A ($\pm 50 \mu$m), B ($\pm 75 \mu$m), C ($\pm 100 \mu$m) and F (outside).

2) Glue layer thickness and tile flatness: Measurement of the glue layer thickness underneath the tiles is performed in order to check the quality of the deposition process and for later comparison with the module thermal performance. As a by-product of this test, the flatness of the tiles is also measured. Measurement of the glue is similar to the substrate flatness, the thickness is obtained through a two-step fitting of both sensor surface and the surrounding substrate area. Resulting
The glue thickness is estimated as a difference between the two, taking the thickness of the tiles of 445 μm into consideration.

Results from one of the measurements are in figure 6. The non-uniformity of the glue layer is caused by the difference between the planes of the micro-channel substrate and the tile, the gradient pointing in the outward direction from the centre of the substrate corresponds to the profile measured during the substrate flatness.

Grading of both tile flatness and glue layer thickness is done by comparing μ ± 2·σ with selected ranges. For the tile flatness the ranges are defined as A (±20 μm), B (±40 μm), C (±60 μm) and F (outside). The glue thickness ranges are A (from 40 μm to 120 μm), B (from 30 μm to 150 μm), C (from 20 μm to 180 μm) and F (outside).

3) Tile position: The exact position of all tiles is crucial for a better starting point of the alignment in the experiment. The measurements are performed using the markers on ASICs and on the back side of the sensor, for the tiles which overhang the cooling substrate. This enables comparison of both sides with respect to each other.

Results of the measurement compared to the ideal position from the design are plotted in figure 7. Comparing distances of the pairs of markers on the same tile enables estimation of the device measurement error.

Grading of the test is done using both absolute values of differences between measured and ideal position Δx and Δy. A (< 30 μm), B (< 45 μm), C (< 60 μm), F (outside).

4) Pull test: To check the quality of wire-bonding, each ASIC contains 4 bonds which are not electric connections. Half of the sacrificial bonds are pulled and breaking forces recorded before finishing the module, the other half is pulled after all testing steps, before shipping to the VELO assembly site.

Grading of the test is defined by breaking force of the bonds in grams, A (all > 5 g), B (any from 2 g to 5 g), C (any < 2 g), F (multiple failed).

B. Mechanical performance

1) IV curves: As the simplest way to check the quality of a known silicon sensor, IV curves of all modules are taken at different steps of production. The IV measurement always contains a level of variation due to the environment therefore the results are only monitored in order to spot large defects in performance.

Comparison of data taken after bump-bonding at Advacam, probe-station at CERN and of the full system with assembled module at Nikhef is in figure 8.

2) Displacement: During installation, evacuation and cooling of the module, the position of the module can change in the z beam direction. The displacement is caused by different thermal coefficients or stress of the connecting materials.

Understanding this shift is crucial as it can affect the performance of reconstructing the particle tracks in the experiment. The final position of the modules will be measured during assembly of the whole VELO at Liverpool, however, the displacement of every module is checked for each step of the installation. This is needed to make sure the modules will not deform too much or get close to the RF foil on either side.

Displacement is measured using capacitive sensors at the tip of the tiles and together with the temperature of the liquid CO₂ monitored as a function of time. Results are in figure 9. These measurements show maximal variation of around 80 μm at the tip of the module, consistent for all built modules and even during multiple and independent cooling-down cycles.

Grading is defined according to the maximal displacement of any of the sensors, A (< 100 μm), B (< 150 μm), C (< 200 μm), F (outside).

3) Thermal cycling: To spot any early deaths of the modules at the experiment, a thermal cycling of 10 cycles is performed, ranging from −30°C to 20°C. At the end, a thermal shock of a rapid temperature increase is performed...
4) Thermal performance: The measurement allows for testing the thermal performance of the module and hence the quality of the glue layer underneath the tiles. The results are projected towards the future increase of power up to 25 W as the cooling plant is limited by amount of heat it can remove.

The ASIC band-gap values, power consumption and temperatures measured by the NTCs on both front-end hybrids and cooling pipes are recorded. A temperature difference \( \Delta T \) between delivered liquid CO\(_2\) and ASICs is monitored as a function of tiles power consumption. The dependency for modules measured at CERN and Manchester is in figure 10.

Since the power consumption of the ASICs will change differently according to how close they are to the interaction point, a more complex grading scheme has been developed. The grading is based on \( \Delta T \) at 25 W, values set as A (all ASICs < 5 °C), B (all near beam ASICs < 5 °C), C (at most one near beam ASIC < 7 °C), F (any ASIC > 7 °C).

C. Communication test

1) TFC (timing and fast control): TFC is the LHCb-wide synchronous control system, providing information every proton bunch crossing what is to be done. The system can tell the front-end to send out a predefined-pattern sync frames, which are detected in the back-end and used to estimate the latency thus synchronise all links of each sub-detector, and also all of the sub-detectors together. Other commands include vetoes, synchronous rests and more.

The performed tests check the response of the system to different commands, making sure the the communication between front-end and back-end is as designed.

2) Equalization: In order to obtain consistent response to the signal in every pixel, the individual pixel threshold has to be adjusted. For this purpose the internal logic contains a 4 bit trim DAC (each threshold DAC corresponding to the signal of 10 e\(^{-}\)) [6], the whole ASIC response is equalized using the per-pixel trim setting. Another important aspect of the equalization is to recognize and mask any noisy or bad pixels, suppressing false signal and flooding of the readout.

The results are in figure 11, visualizing both process of equalization and final trim DAC mapping to the tile.

3) PRBS (pseudo-random bit stream): The operation of the whole VELO requires that data are successfully transferred without any significant deterioration. To ensure good connections and pull phase settings, a PRBS test is performed, checking that the BER (bit error rate) is less than a defined value in all of the 20 links used to read out the data. For this purpose a stream of predefined numbers is generated in each ASIC and sent to the DAQ, where the received bits are compared and error rate is estimated, results shown in figure 12.

Grading of the test is defined using the BER values, results being A (all links < 10\(^{-12}\)), B (any links on a different readout tape > 10\(^{-12}\)), C (any links on the same readout tape > 10\(^{-12}\)) and F (any links > 10\(^{-10}\)).

V. Conclusion

As the precision requirements for the VELO upgrade modules are high, different techniques were developed in order to achieve the best quality and longevity of the modules. The information about every production step and state of each module is uploaded to a dedicated online database, where scripts analyze data for further automatic grading.

After the module production is finished and the whole VELO is assembled in Liverpool in the first half of 2020, work for the installation and commissioning of the whole system will start at CERN. The system should be ready for the data-taking in the early 2021.
Fig. 12: Results of the PRBS test.

REFERENCES


