The CMS Tracker Upgrade for the High Luminosity LHC

Giacomo Sguazzoni on behalf of the CMS tracker group

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Abstract—The LHC machine is planning an upgrade program which will smoothly bring the luminosity to about $5 - 7.5 \times 10^{34} \text{ cm}^{-2} \text{s}^{-1}$ in 2028, to possibly reach an integrated luminosity of $3000 - 4500 \text{ fb}^{-1}$ by the end of 2039. This High Luminosity LHC scenario, HL-LHC, will require an upgrade program of the LHC detectors known as Phase-2 upgrade. The current CMS Outer Tracker [1], already running beyond design specifications, and CMS Phase-1 Pixel Detector [2] will not be able to survive HL-LHC radiation conditions and CMS will need completely new devices, in order to fully exploit the highly demanding conditions and the delivered luminosity. The new Outer Tracker should have also trigger capabilities. To achieve such goals, R&D activities have explored options for both the Outer Tracker and for the Inner Tracker. The solutions developed will allow to include tracking information in the first level trigger stage. The design choices for the Tracker upgrades are discussed along with some highlights on technological approaches and R&D activities.

I. INTRODUCTION

The High Luminosity phase of the LHC (HL-LHC) is expected to start in 2026. With an instantaneous luminosity of $5 - 7.5 \times 10^{34} \text{ cm}^{-2} \text{s}^{-1}$ already available in 2028, the aim is to collect between 3 ab$^{-1}$ and 4.5 ab$^{-1}$ of integrated luminosity per experiment in about ten years. The HL-LHC will operate at a centre-of-mass energy of 14 TeV with a bunch crossing spacing of 25 ns. The number of pile-up (PU) collisions will reach an average of $\sim 140$, increasing to $\sim 200$ in the best performance scenario. The radiation levels will be unprecedented, especially in the innermost layers of the detector, where a fluence of $2.3 \times 10^{15} (1 \text{ MeV n}_{\text{eq}})/\text{cm}^{2}$ and a total ionizing dose of 12 MGy (1.2 Grad) are foreseen for the baseline integrated luminosity of 3 ab$^{-1}$. The present Outer Tracker [1] and Phase-1 Pixel detector [2] would be unable to cope with the HL-LHC environment [3] due to the significantly higher rate, the longer latency of the upgraded trigger system, that is crucial for operation at high luminosity, and the fatal consequences of the irradiation. The entire Tracker will thus be replaced [4] during the long shutdown in 2024-2025 (LS3) when also LHC will be upgraded. The new Tracker, similar to the current one, will be made up of two subdetectors: the Outer Tracker (OT) and the Inner Tracker (IT).

Despite the foreseen improvements, the muon and calorimeter-based lowest level trigger (Level-1 or L1) will not be able to cope with the contamination due to ghosts generated by the PU and the limited resolution: e.g., in the muon case, no transverse momentum ($p_{t}$) threshold is sufficiently effective in reducing the rate. The Tracker is then required to provide high-resolution, high-purity, high- $p_{t}$ tracks to the L1 system.

The main requirements for the Tracker upgrade [3] can be summarized as follows:

- radiation tolerance and cold (−20$^\circ$C) operation to be functional up to 3 ab$^{-1}$ (with margins to comply with the best performance scenario);
- increased granularity (occupancy $< \mathcal{O}(1\%)$ for the OT and $< \mathcal{O}(0.1\%)$ for the IT) and optimized layout for robust pattern recognition;
- reduced passive material;
- track trigger capabilities to contribute to L1;
- large readout bandwidth and deep front-end buffers for higher rate (750 kHz) and the long latency (12.5 $\mu$s) of the upgraded L1 trigger system;
- extended coverage up to $|\eta| \sim 4$ for efficient PU mitigation and better physics objects reconstruction in the forward region;
- very forward part of IT usable as a luminosity monitor;
- IT fully accessible for maintenance and part replacement.

The layout of the upgraded Tracker is shown in Fig. 1.

II. THE INNER TRACKER

The Inner Tracker consists of $\sim 4.9 \text{ m}^2$ of silicon pixel detectors for a total of 2G readout channels. The IT is build up of two types of hybrid pixel modules with $1 \times 2$ or $2 \times 2$ readout chips (ROC), organized into four barrel layers (TBPX) plus eight small disks (TFPX) and four large disks (TEPX) per side (see Fig. 2). A quarter of the mechanical implementation of the IT is shown in Fig. 3.

Fig. 1. The layout of the upgraded tracker with labels indicating the various subsystems. Red and blue lines represent OT 2S and PS modules respectively. Yellow and green lines represent IT $2 \times 2$ and $1 \times 2$ modules respectively.
A. IT Pixel Sensors and Readout Chip

Thin planar n-in-p sensors, with $100 - 150 \mu m$ maximum active thickness, have been chosen for the IT to have more stable charge collection efficiency after irradiation [5]. Nevertheless they need rather high bias voltage (up to $0.8 - 1 kV$) and spark protection between the ROC and the sensor. The alternative option of 3D sensors is still under consideration for the innermost layer since this kind of sensor is potentially more rad-hard, but requires a more complex fabrication and has larger cell capacitance [5]. A sketch illustrating the different concept of the two options can be seen in Fig. 4.

Two possible pixel aspect ratios are being scrutinized for the IT: $25 \times 100 \mu m^2$ (current baseline) and $50 \times 50 \mu m^2$. The bond pad pattern stays the same as shown in Fig. 5.

The ROC needed for the IT is a technology frontier device that needs to withstand 1.2 Grad of total ionising dose and 3.2 GHz/cm$^2$ of maximum hit rate. It is being developed by the RD53 collaboration [6] in 65nm technology and features a $50 \times 50 \mu m^2$ elementary cell, low threshold (<1000e$^-$), high hit and trigger rate (thanks to up to four 1.28 Gb/s output links), radiation resistance and serial powering capabilities [7].

RD53A is the first prototype (about half in size with respect to the foreseen final chip) and it is also being used for the IT R&D. The final chip submission is foreseen in June 2019. Figure 6 shows a schematic layout of the ShuntLDO, the heart of the serial powering capability of the ROC [7]. A simplified floorplan of the RD53A prototype is shown in Figure 7.
B. The IT Pixel modules

The IT is based on pixel sensor modules in two flavours, \( 1 \times 2 \) and \( 2 \times 2 \) chips, shown in Fig. 8. Each module is build up by the sensor, bump-bonded to the ROC chips, by a High-Density Interconnect (HDI) to distribute signals and power and additional minimal elements (e.g. AlN rails) for fixture onto the support structure and to ensure sufficient mechanical strength. No extra ASICs other than the ROC are needed.

The modules are arranged in groups of 8 to 12 into a serial powering chain (sketched in Fig. 9) since this is the only viable approach by which it is possible to feed the 50 kW needed by the IT while minimizing the passive material.

C. IT Luminosity measurement

Thanks to special extra triggers, TEPX will serve as the main online bunch-by-bunch luminometer for CMS. This functionality will require \( \sim 10\% \) more triggers during normal data taking. As a luminometer TEPX will be operated also before LHC beams are declared stable; in this case all the readout bandwidth is available for the luminosity measurement for which several methods are under study:

- cluster counting that is characterized by a high statistics but suffers from large fake rate;

- multi-hit stub counting using overlaps that would allow for a reduction of the fake rate still maintaining a fairly large statistics;

- track counting that, however, needs processing in the back-end processors.

III. THE OUTER TRACKER

As visible in Fig. 1, the Outer Tracker (OT) is organized in six barrel layers and five endcap disks per side that build up the TEDD subsystem. The barrel subsystems are the TBPS and the TB2S, the three innermost and outermost barrel layers, respectively. An innovative ‘tilted’ geometry is used in the TBPS to optimize the active silicon surface usage with respect to performance.
PS modules are deployed in the radial range between 20 cm and 60 cm, i.e. the inner region (blue in Fig. 1). The macro-pixels precise measurements of the $z$ coordinate is crucial for primary vertex discrimination at L1 and for a robust pattern recognition.

Overall the OT consists of 13296 modules for a total of $\sim 192 m^2$ of Si sensors with 42M strips and 170M macro-pixels.

B. OT sensors

Depending on type and final position, OT sensors will be exposed to particle fluences up to $1.5 \cdot 10^{15} (1 \mathrm{MeV n_{eq}}) \cdot \mathrm{cm}^{-2}$ and three types of sensors will be used: 5 cm strips with 90 $\mu$m for the 2S modules, 2.3 cm with 100 $\mu$m strips for PS modules and 1467 $\mu$m macro-pixels with 100 $\mu$m pitch for the PS modules. These sensors will be n-in-p fabricated out 6” wafers with active thicknesses between 200 and 300 $\mu$m. Final decision on material, thickness and isolation technique is on-going. The nominal bias voltage will be 600 V with the possibility to increase up to 800 V. As an example, Fig. 13 shows the cluster seed-strip signal as a function of the bias voltage for 2S sensor prototypes for HL-LHC fluence figures.

C. OT Off-Detector Electronics

The OT DAQ, controls and power supply chain is sketched in Fig. 14 where the functional elements of the back-end system are also shown. The Data, Trigger & Control Board (DTC) sends and receives data from 72 input fibres. This board is a custom development based on off-the-shelf commercial FPGAs and multi-channel optoelectronic transceivers. It processes three detector data streams: Timing and Control, DAQ and Trigger with a global usable bandwidth of 720 (180) Gb/s from (to) detector. The DTC board sends the high-$p_t$ track data selected by the $p_t$-modules, i.e. the stub stream, to the L1 track finding system with 25 Gb/s bandwidth [8].

The OT will require $\sim 100 kW$ of power that will be supplied to the FE at a voltage of 10 – 12 V by a system that will likely be located in the experiment balconies. The power supply will also provide up to 800 V of bias voltage with up to 2 mA to the modules. The current design foresees individual supply lines to each detector module that would best match the Tracker modularity and alleviate the need for complex fail-safe redundancy schemes.

D. OT mechanical structure

The mechanical concept of the Phase-2 Tracker is the same as for the current Tracker and consists of a support tube at the outermost radius and smaller substructures.

The 2S modules that equip the TB2S are mounted onto ladders (see Fig. 15), 12 modules each, then installed in a support wheel that consists of four vertical disks joined by cylinders at the inner and outer radii.

The PS modules that equip the central part of the TBPS, shown in Fig. 16, are glued directly onto flat composite structures in which the cooling pipes are embedded. Figure 17
instead shows the ring structure that enables for the ‘tilted’ layout concept.

In the endcaps the modules are mounted on flat disks, as shown in Fig. 18. These are split in half-disks, or “dees”. Two disks are grouped to form one Double-Disk; ten Double-Disk units will be produced, five for each end-cap or TEDD.

IV. COMMON ASPECTS

A. Cooling

The Tracker will be operated and maintained well below $-20^\circ C$ using a two-phase CO$_2$ cooling system running at $-30^\circ C$. The total power dissipated by the Tracker within the tracking volume is estimated to be around $\sim 100$ kW for the OT and $\sim 50$ kW for the IT. As sketched in Fig. 19 the cooling system relies on five identical 50 kW cooling plants. For redundancy, one is kept always in stand-by ready to take over if needed.

B. Data Flow

The readout and control system is schematically represented in Fig. 20. The OT, via the DTC, will feed L1 Track Trigger processors with track trigger data at 40 MHz. OT and IT will be fully readout on the receipt of a L1 accept signal at a maximum rate of 750 kHz. As above explained, in parallel, special L1 ‘lumi’ triggers will be issued to IT TEPX for instantaneous luminosity measurements. It is estimated that these triggers should have a 75 kHz rate to ensure sufficient statistics for the measurements.

C. Material Budget

The upgrade project aims at a significant reduction of the impact of the tracker material with respect to the current Phase-1 Tracker. In fact, overall, the estimated material budget of the upgraded Tracker shows a significant reduction, in particular around $|\eta| = 1.5$, as visible in Fig. 21 where the Phase-2 vs. Phase-1 comparison is shown.
D. Performance

Some highlights of the expected offline tracking performance [4] are discussed. The track reconstruction efficiency for the Phase-2 detector at a PU of 140 and 200 for simulated $t\bar{t}$ events, shown in Fig. 22 for $t\bar{t}$ together with the expected fake rate, is as good as the Phase-1 efficiency at a PU of 50 events and extends the $|\eta|$ range by about 1.5 units thanks to the foreseen extension of the pixel detector. The fraction of reconstructed ghost tracks is higher but still adequate for the CMS event reconstruction.

The impact of the much better granularity and the reduced passive material on physics performance is well visible in Fig. 23 where the resolution on $p_t$ and the transverse impact parameter are reported as a function of the pseudorapidity in comparison between the Phase-1 and upgraded Phase-2 tracker detectors.

The ability to perform track reconstruction in dense environment as the jet core is an important figure of merit for a tracking device at a pp collider. The resolutions of jet-related observables very much depend on the possibility to disentangle, e.g. by using vertex information, the tracks belonging to the jet with respect to PU tracks accidentally close-by. The clear improvement in this respect is visible in Fig. 24 where the track reconstruction efficiency as a function of the distance $\Delta R = \sqrt{(\Delta \eta)^2 + (\Delta \phi)^2}$ from the jet axis is shown for the Phase-1 and upgraded Phase-2 tracker detectors.

V. CONCLUSIONS

The Phase-2 Tracker upgrade for HL-LHC consists of a replacement of the whole Tracker detector. The development of the new Tracker is well advanced. The detector layout is being optimized and finalized and several R&D activities are ongoing on sensors, electronics and mechanics. The project will soon enter into parts production and integration.

REFERENCES

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